

DENON

Handy Hint

SHARC® DSP

SHARC® DSP Processors, (*Super Harvard Architectural*) dominate the floating-point Digital Signal Processing market, delivering exceptional core and memory performance.

The industry standard SHARC® floating point family makes processors for applications where dynamic range is a key consideration such as home and professional audio products.

The third generation of SHARC® Processors offers increased performance, audio and application-focused peripherals, and new memory configurations capable of supporting the latest surround-sound decoder algorithms.



The ADSP-21367 increases performance up to 400MHz while simplifying algorithm development with the integration of a high-bandwidth and very flexible external memory interface.

The ADSP-21367 is based on a single-instruction, multiple-data (SIMD) core which supports both 32-bit fixed-point and 32-/40-bit floating point arithmetic formats and is completely code-compatible with all prior SHARC Processors.